Prisemi®

PESDNC3D18VUL

ESD Protector

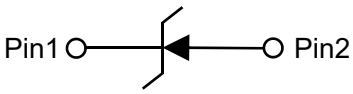
Description

The PESDNC3D18VUL protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

SOD-323(Top View)

Feature

- 264W peak pulse power per line (t_P = 8/20μs)
- SOD-323 package
- Unidirectional configurations
- Response time is typically < 1 ns
- Protect one I/O or power line
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to
  IEC 61000-4-2(ESD) ±30kV(air), ± 30kV(contact);
  IEC 61000-4-5 (Lightning) 8A (8/20us)



Circuit Diagram

Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

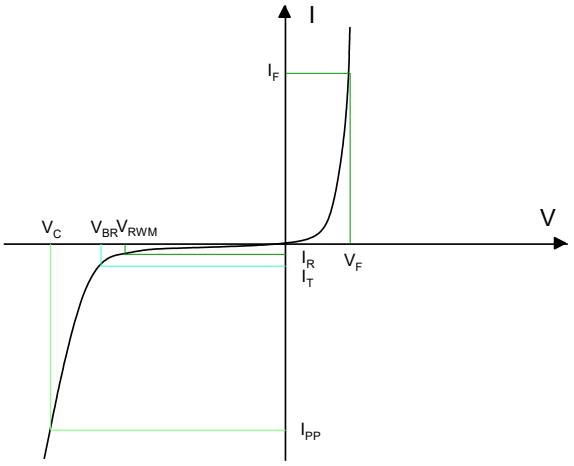
Marking (Top View)

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260℃
- Pure tin plating: 7 ~ 17 um

Electronics Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_J	Junction Capacitance
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}	-	-	-	18	V
Forward Voltage	V_F	I_F = 10mA	-	0.8	1.1	V
Breakdown Voltage	V_{BR}	I_t = 1mA	19	22	25	V
Reverse Leakage Current	I_R	V_{RWM} = 18V	-	-	1.0	μA
Clamping Voltage[1)]	V_C	TLP = 16A, t_p = 100ns	-	26	-	V
Dynamic resistance[1)]	R_{DYN}	-	-	0.25	-	Ω
Clamping Voltage[2)]	V_C	I_{PP} = 5A,t_P = 8/20μs	-	28	31	V
		I_{PP} = 8A,t_P = 8/20μs	-	33	36	V
Junction Capacitance	C_J	V_R = 0V,f = 1MHz	-	45	60	pF

Notes:
1.TLP parameter: Z_0=50Ω, t_p=100ns, t_r=2ns, averaging window from 70ns to 90ns. R_{DYN} is calculated from 4A to 16A.
2.Non-repetitive current pulse, according to IEC61000-4-5.

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t_P = 8/20μs)	P_{PP}	264	W
Peak Pulse Current (t_P = 8/20μs)	I_{PP}	8.0	A
Lead Soldering Temperature	T_L	260 (10 sec)	℃
Junction and Storage Temperature Range	T_J,T_{STG}	-55~+150	℃
ESD Protection-Contact Discharge	V_{ESD}	±30	kV
ESD Protection-Air Discharge	V_{ESD}	±30	kV

Typical Characteristics

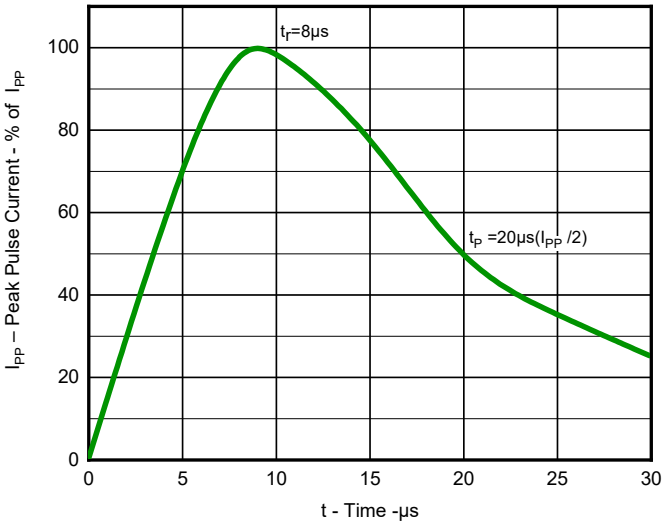


Fig 1.Pulse Waveform(8/20μs)

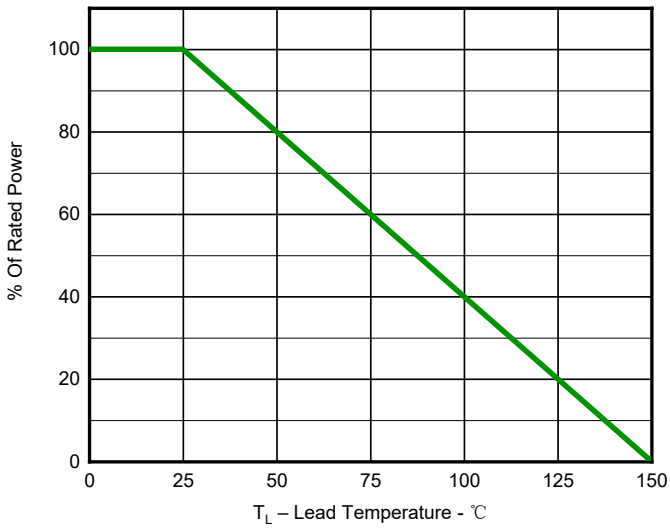


Fig 2.Power Derating Curve

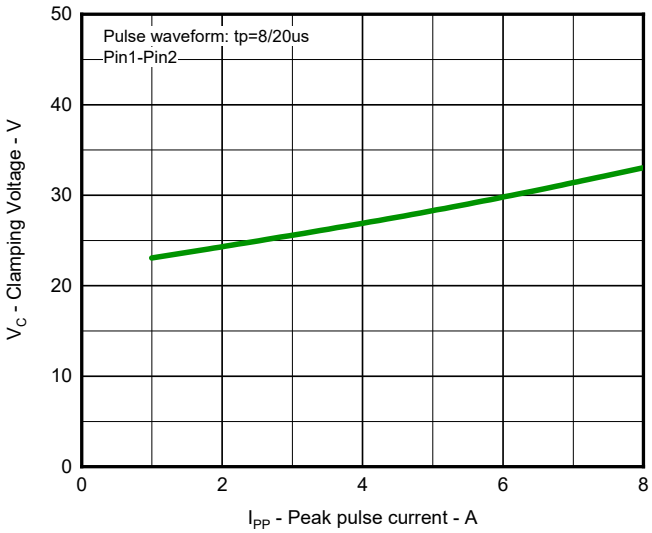


Fig 3. Clamping voltage vs. Peak pulse current

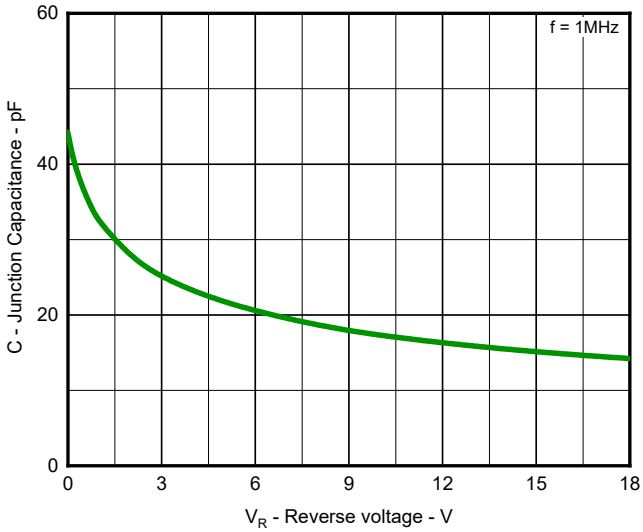


Fig 4. Capacitance vs. Reveres voltage

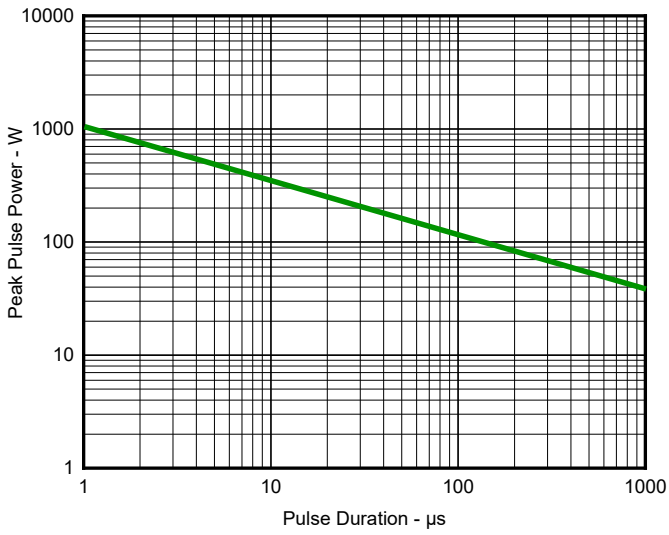


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

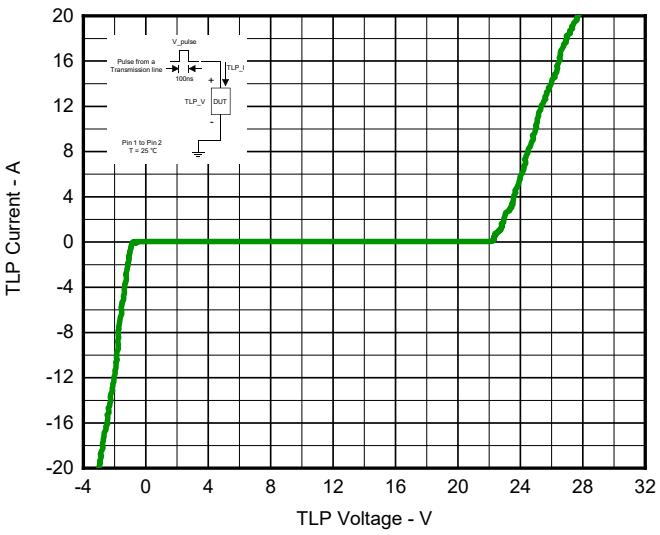


Fig 6. TLP Measurement

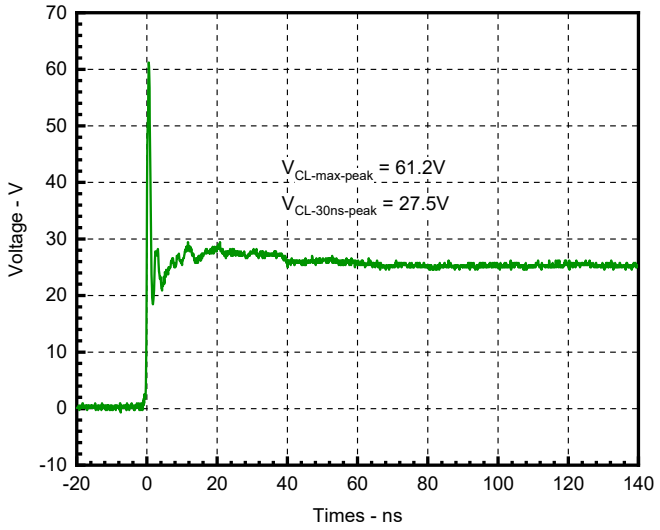


Fig 7. Clamping Voltage at IEC61000-4-2 +8kV Pulse Waveform

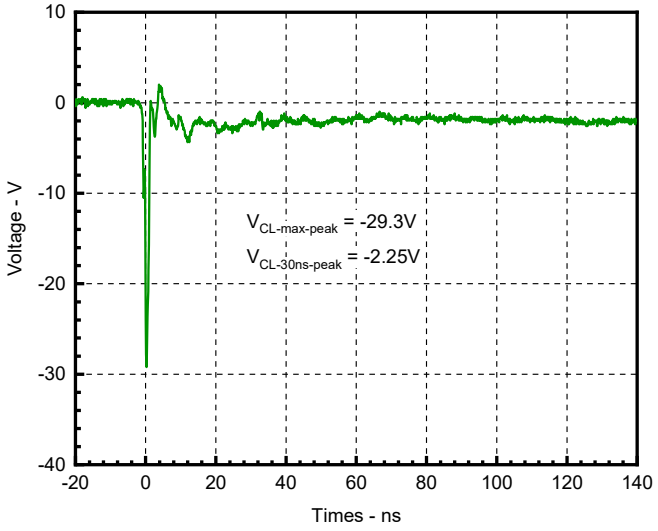
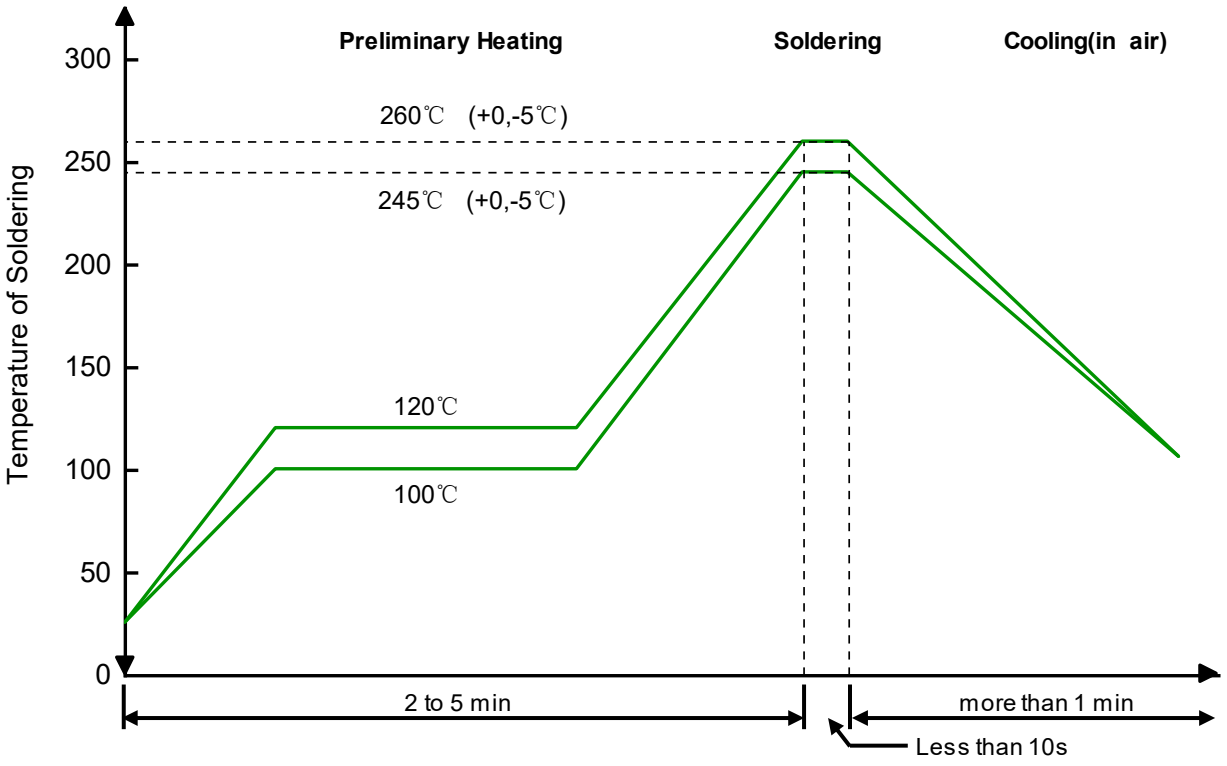


Fig 8. Clamping Voltage at IEC61000-4-2 -8kV Pulse Waveform

Solder Reflow Recommendation



Remark: Pb free for 260℃; Pb for 245℃.

PCB Design

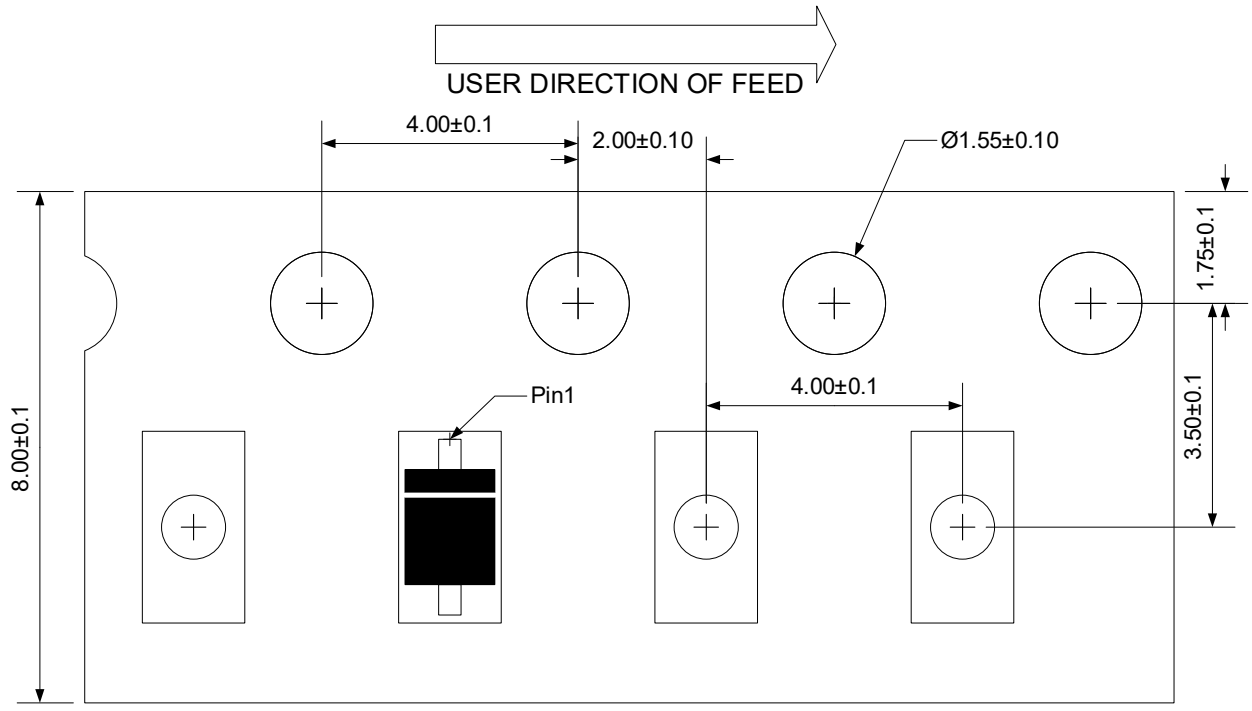
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Ordering information

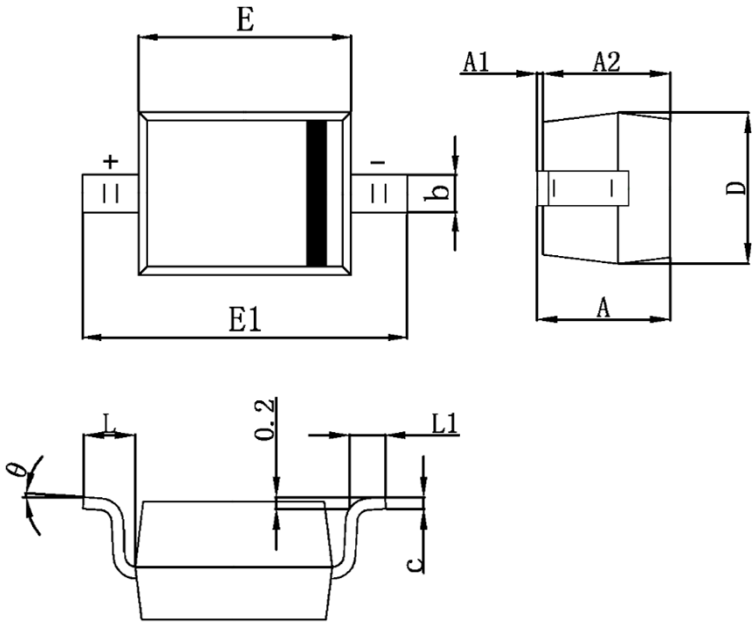
Device	Package	Reel	Shipping
PESDNC3D18VUL	SOD-323 (Pb-Free)	7"	3000 / Tape & Reel

Load with information

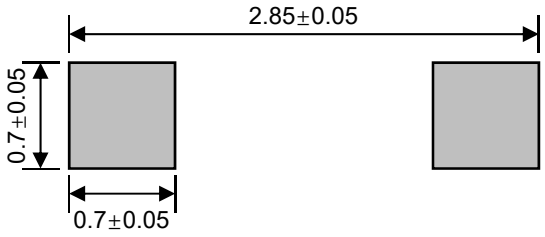


Unit:mm

Product dimension (SOD-323)



Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.800	1.000	0.031	0.039
A1	0.000	0.100	0.000	0.004
A2	0.800	0.900	0.031	0.035
b	0.250	0.350	0.010	0.014
c	0.080	0.150	0.003	0.006
D	1.200	1.400	0.047	0.055
E	1.600	1.800	0.063	0.071
E1	2.550	2.750	0.100	0.108
L	0.475 Ref.		0.019 Ref.	
L1	0.250	0.400	0.010	0.016
θ	0°	8°	0°	8°



Unit:mm

Suggested PCB Layout

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