

Description

The P14C2ND is an Over-Voltage-Protection (OVP) load switch with fixed OVLO threshold voltage. The OVLO threshold voltage is fixed 6.0V. The device will switch off internal MOSFET to disconnect IN to OUT to protect load when any of input voltage over the threshold. The Over temperature protection (OTP) function monitors chip temperature to protect the device. The OCP function turns off OUTPUT if the load current is over the threshold and recovers when VIN re-plug. The OCP current limit threshold is adjustable by an external R_{ILIM} .

The P14C2ND is available in DFN1.2x1.2-4L. Standard products are Pb-free and Halogen-free.

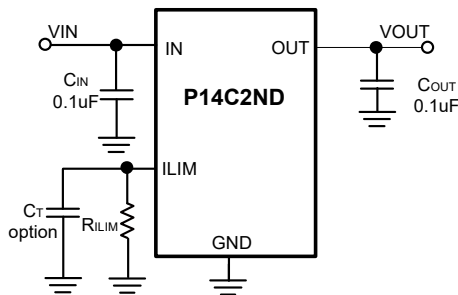


Figure 1: Typical Application

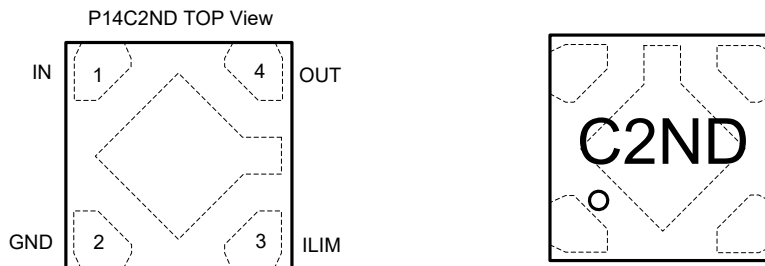


Figure 2: Pin Configuration(DFN1.2x1.2-4L) and Marking (Top view)

Feature

- Maximum input voltage : 32V
- Ultra fast OVP response time: 50ns (Typ.)
- Fixed OVLO threshold voltage: 6.0V, $\pm 3\%$
- Adjustable over-current protection:
100mA-1.8A
- Supports up to 1.5 A Load Current
- Thermal Shutdown
- Available in Green DFN1.2x1.2-4L Package

Application

- Mobile Handsets and Tablets
- Portable Media Players
- Low-Power Handheld Devices

Pin Definitions

Pin No.	Symbol	Descriptions
1	IN	Switch Input and Device Power Supply.
2	GND	Ground Terminal. Connect to the thermal pad and to the ground rail of the circuit.
3	ILIM	Current limit adjustment. Connect a resistor to GND to set over current threshold. $I_{Lim} = 25/R_{ILIM}$. (current in A, resistance in k Ω) Short ILIM to GND will disable current limitation.
4	OUT	Switch output Terminal to the Charging System.

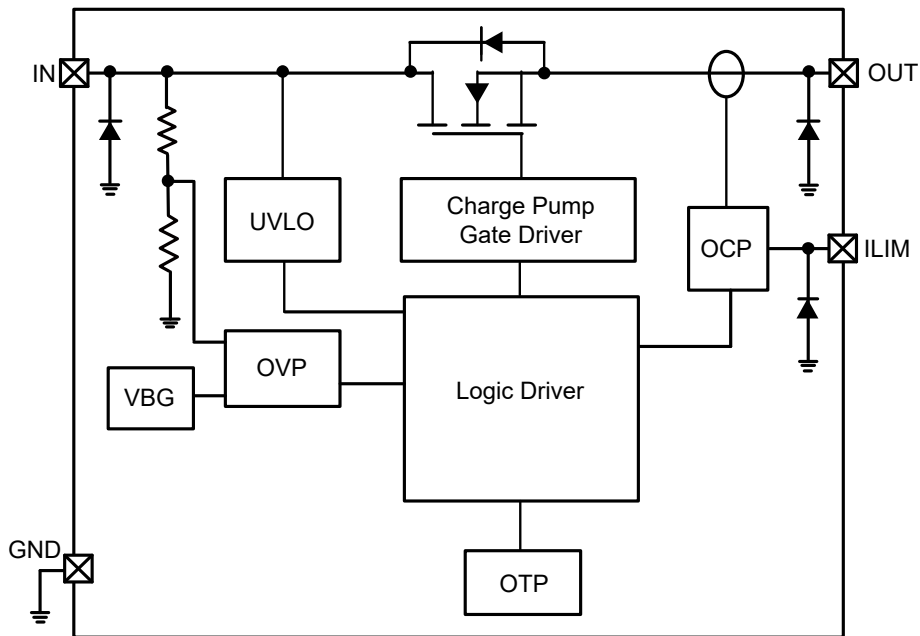


Figure 3: IC Block Diagram

Absolute maximum rating

Parameter(Note1)	Symbol	Value	Units
Input voltage (IN pin)	V_{IN}	-0.3 ~ 32	V
Output voltage (OUT pin)	V_{OUT}	-0.3 ~ 6.0	V
Junction temperature	T_J	150	°C
Lead temperature(10s)	T_L	260	°C
Storage temperature	T_{stg}	-55~150	°C

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Parameter	Symbol	Value	Units
Input voltage	V_{IN}	3.5~32	V
MAX Continuous Output current	I_{OUT}	1.5	A
Ambient operating temperature	T_{opr}	-40~85	°C

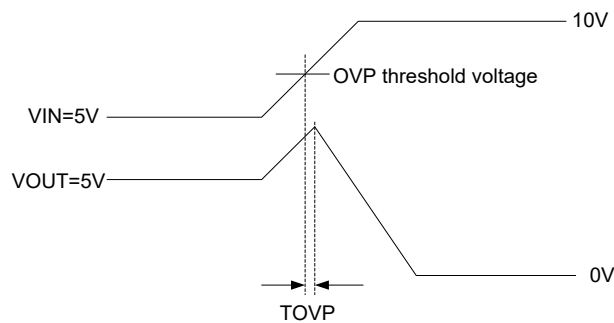
Over voltage protector

Electrical Characteristics

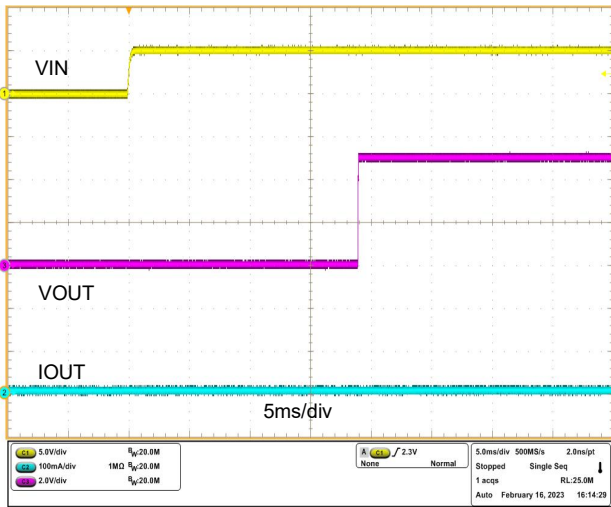
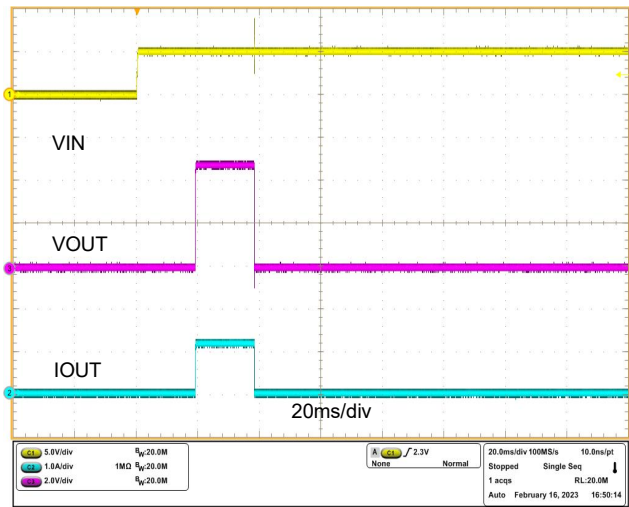
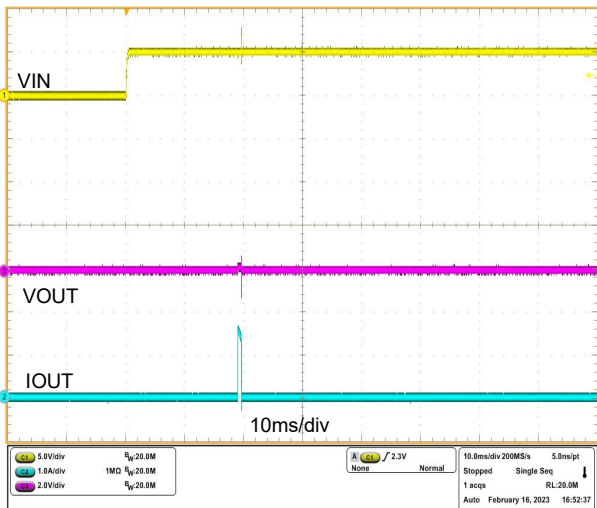
($T_A=25^{\circ}\text{C}$, $V_{IN}=5\text{V}$, $C_{IN}=0.1\mu\text{F}$, $C_{OUT}=0.1\mu\text{F}$, $R_{LIM}=24\text{K}\Omega$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Input voltage range	V_{IN}		3.5		32	V
Quiescent current	I_Q	NO Load, /CE=GND, $V_{IN}=5\text{V}$		120	200	μA
Over voltage quiescent current	I_{Q_OVP}	NO Load, /CE=GND, $V_{IN}=30\text{V}$		200		μA
Drop Voltage from IN to OUT	V_{DROP}	$V_{IN}=5\text{V}$, $I_{OUT}=0.5\text{A}$		85		mV
OVP response time	t_{OVP}	V_{IN} rising, $C_{IN}=C_L=0\text{pF}$ (Note2)		50		ns
OVP voltage	V_{OVLO}	V_{IN} rising	5.82	6.0	6.18	V
UVLO threshold voltage	V_{UVLO}	V_{IN} rising		2.35		V
UVLO hysteresis voltage	V_{UVLO_HYS}	V_{IN} falling		25		mV
OCP setting range	$IOCP_RANG$		100		1800	mA
OCP Accuracy	$IOCP_ACY$	$IOCP_SET \leq 200\text{mA}$		± 30		mA
		$IOCP_SET = 300\text{mA}$		± 15		%
		$IOCP_SET \geq 500\text{mA}$		± 10		%
Debounce Time	T_{DEB}	$V_{IN} > V_{UVLO}$ to $V_{OUT} = V_{IN} * 10\%$	10	18	30	ms
Turn On Time	T_{ON}	$V_{OUT} = V_{IN} * 10\%$ to $V_{OUT} = V_{IN} * 90\%$		40		μs
OTP threshold temperature	T_{OTP}	$V_{IN}=5\text{V}$		150		$^{\circ}\text{C}$
OTP hysteresis temperature	T_{HYS}	$V_{IN}=5\text{V}$		20		$^{\circ}\text{C}$

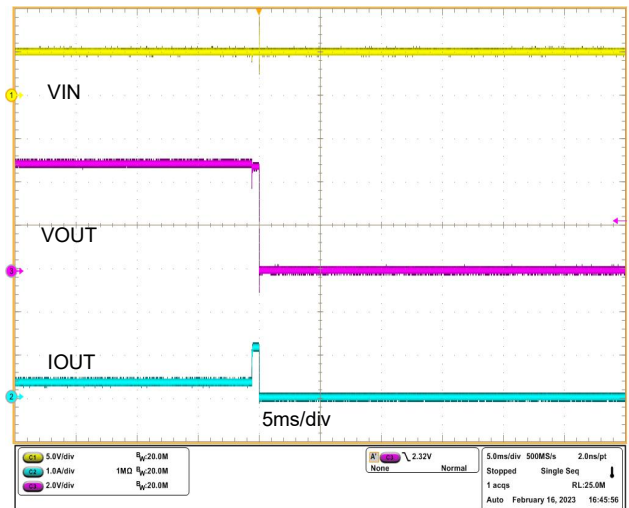
Note 2: Guaranteed by design



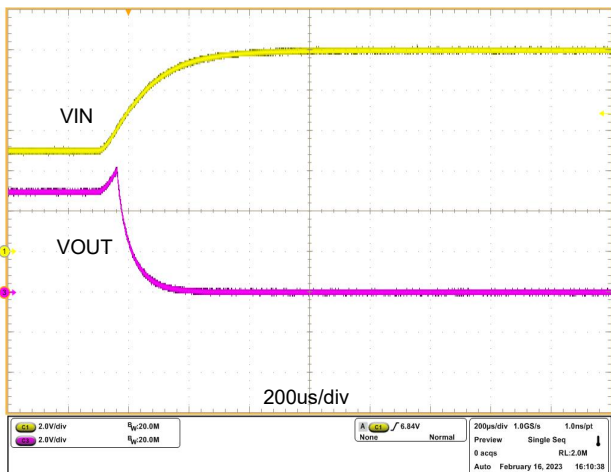
OVP response time test

Typical Operating Performance

 Power on Response(No Load, $R_{LIM}=24k\Omega$)

 Power on OCP Response($R_{out}=4\Omega$, $R_{LIM}=24k\Omega$)


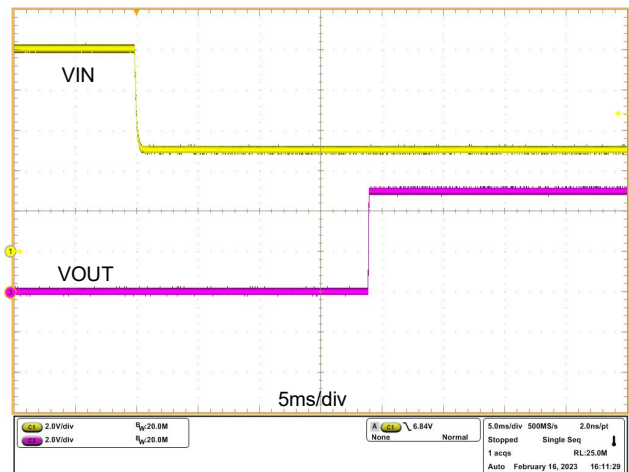
Power on Response with Output Short



OCP Response



OVP Response



OVP Recovery

Function Descriptions**1. Under-voltage Lockout (UVLO)**

The under-voltage lockout (UVLO) circuit disables the power switch until the input voltage reaches the UVLO turn on threshold. Built-in hysteresis prevents unwanted on and off cycling because of input voltage droop during turn on.

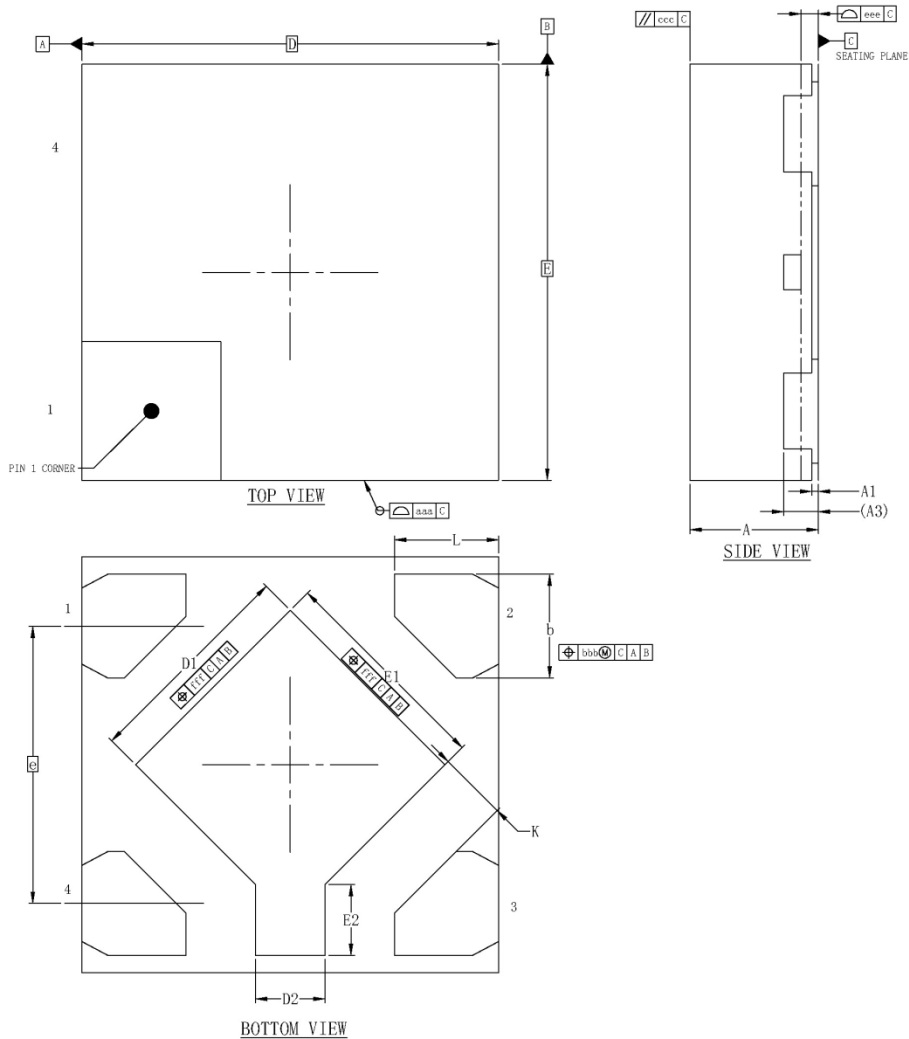
2. Over Current Protection (OCP)

If the load current rises to the OCP threshold, the device will cut off the output voltage. It takes 18ms after power on for OCP begins to detect. After Power Good, the OCP active time is dozens to hundreds microseconds.

The OCP threshold is calculated by the equation: $I_{LIM} = 25/R_{LIM}$ (current in A, resistance in k Ω).


3. Over-voltage Lockout (OVLO)

When V_{IN} exceeds the OVP threshold voltage, the over-voltage lockout (OVLO) circuit turns off the protected power switch.

Product dimension (DFN1.2X1.2-4L)


Dim	Millimeters		
	MIN	Typ.	MAX
A	0.32	0.37	0.40
A1	0.00	0.02	0.05
A3	0.102REF		
b	0.25	0.3	0.35
D	1.1	1.2	1.3
E	1.1	1.2	1.3
e	0.8BSC		
D1	0.53	0.63	0.73
E1	0.53	0.63	0.73
D2	0.1	0.2	0.3
E2	0.18REF		
L	0.25	0.3	0.35
K	0.2REF		

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