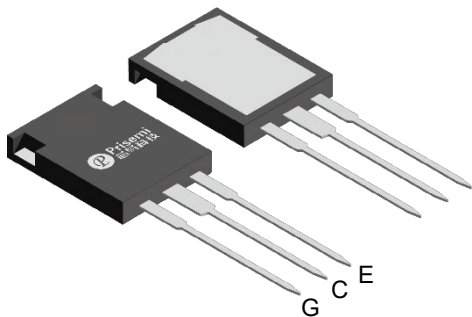
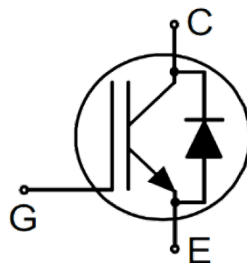
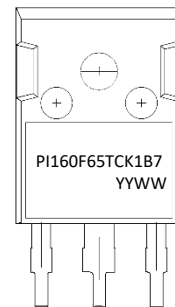


Description

TO-247PLUS-3L

Circuit Diagram

Marking (Top View)
Feature

- Easy paralleling capability due to positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Low Saturation Voltage $V_{CE(sat)}$
- Maximum junction temperature $T_{VJmax}=175^{\circ}C$

Applications

- UPS
- EV-Charger
- Solar String Inverter
- Energy Storage Inverter

Absolute maximum rating@25°C

Parameter	Symbol	Value	Units	
Collector-Emitter Voltage	V_{CE}	650	V	
Gate-Emitter Voltage	V_{GE}	± 20	V	
Transient Gate-emitter Voltage ($t_p \leq 10\mu s, D < 0.010$)		± 30		
Collector Current	I_C	$T_c = 25^{\circ}C$	240*1	A
		$T_c = 100^{\circ}C$	160	
Pulsed Collector Current	I_{CM}	560	A	
Diode Current	I_F	$T_c = 25^{\circ}C$	240*1	A
		$T_c = 100^{\circ}C$	160	
Diode Pulsed Current	I_{FM}	560	A	
Power Dissipation	P_D	$T_c = 25^{\circ}C$	1000	W
		$T_c = 100^{\circ}C$	500	
Operating Junction Temperature	T_{VJ}	-40~+175	$^{\circ}C$	
Storage Temperature	T_{STG}	-55~+150	$^{\circ}C$	

Notes:

- 1.The max Collector current rating is package limited
- 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units	
Collector-Emitter Breakdown Voltage	BV_{CE}	-	650	-	-	V	
C-E Leakage Current	I_{CES}	$V_{CE}=650V, V_{GE}=0V$	-	-	160	μA	
G-E Leakage Current	I_{GES}	$V_{GE}=\pm 20V, V_{CE}=0V$	-	-	100	nA	
Transconductance	g_{fs}	$V_{GE}=20V, I_C=160A$	-	160	-	S	
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	$I_C=0.25mA, V_{CE}=V_{GE}$	4.2	5.2	6.2	V	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=160A, V_{GE}=15V$	$T_{VJ}=25^\circ C$	-	1.37	1.6	V
			$T_{VJ}=125^\circ C$	-	1.63	-	
			$T_{VJ}=175^\circ C$	-	2	-	
Diode forward voltage	V_F	$I_C=160A, V_{GE}=0V$	$T_{VJ}=25^\circ C$	-	1.33	1.6	V
			$T_{VJ}=125^\circ C$	-	1.37	-	
			$T_{VJ}=175^\circ C$	-	1.4	-	
Input Capacitance	C_{ies}	$V_{CE}=25V, V_{GE}=0V, f=1MHz$	-	10203	-	pF	
Output Capacitance	C_{oes}		-	480	-		
Reverse Transfer Capacitance	C_{res}		-	70	-		
Total Gate Charge	Q_g	$V_{CE}=520V, V_{GE}=0V \text{ to } 15V, I_C=160A$	-	330	-	nC	
Gate to Emitter Charge	Q_{ge}		-	51	-		
Gate to Collector Charge	Q_{gc}		-	72	-		
Turn-on Delay Time	$t_{d(on)}$	$V_{CC}=400V, I_C=160A, V_{GE}=15V, R_G=10\Omega, \text{ Inductive Load}$	-	94	-	ns	
Rise Time	t_r		-	136	-		
Turn-off Delay Time	$t_{d(off)}$		-	379	-		
Fall Time	t_f		-	214	-		
Turn-on Energy Loss	E_{on}	$V_{CC}=400V, I_C=160A, V_{GE}=15V, R_G=10\Omega, \text{ Inductive Load}$	-	6.37	-	mJ	
Turn-off Energy Loss	E_{off}		-	9.81	-		
Total Switching Loss	E_{st}		-	16.18	-		
Diode Reverse Recovery Time	T_{rr}	$V_{CC}=550V, I_C=160A, V_{GE}=0/15V, R_G=10\Omega$	-	190	-	ns	
Diode Reverse Recovery Charge	Q_{rr}		-	3.09	-	μC	
Diode Reverse Recovery Current	I_{rm}		-	29	-	A	

Thermal Resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, IGBT Junction-Ambient	$R_{th(J-A)}$	-	-	40	$^{\circ}C/W$
Thermal Resistance, IGBT Junction to Case	$R_{th(J-C)}$	-	-	0.12	$^{\circ}C/W$
Thermal Resistance, FRD Junction to Case	$R_{th(J-C)}$	-	-	0.16	$^{\circ}C/W$

Typical Characteristics

Fig 1. Typical Output Characteristics (25°C)

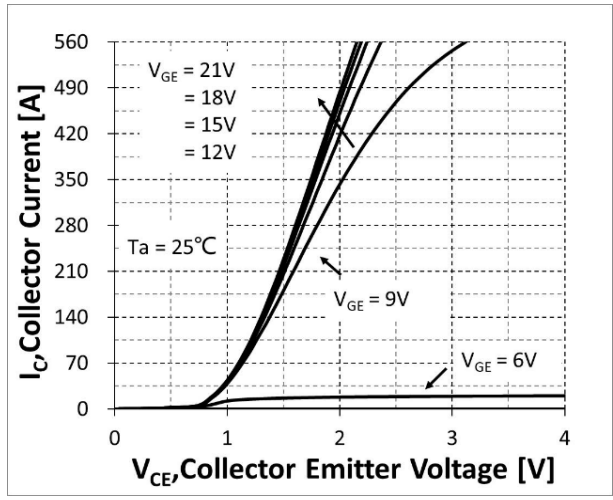


Fig 2. Typical Output characteristics (150°C)

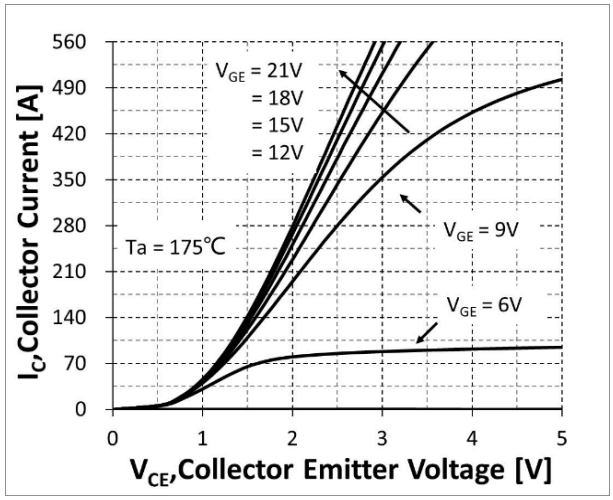


Fig 3. Forward Bias Safe Operating Area

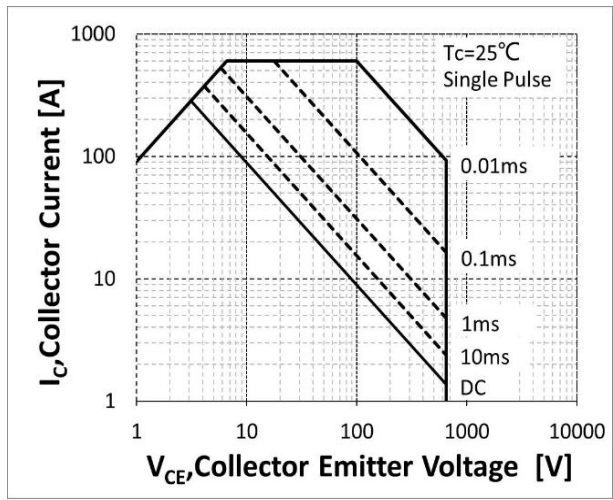
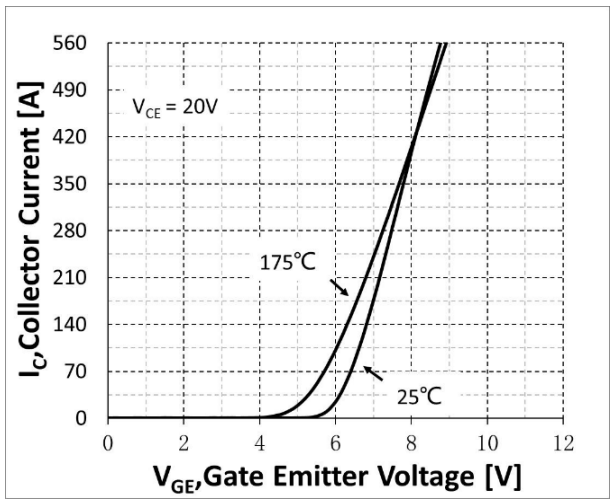


Fig 4. Transfer Characteristics



Typical Characteristics

Fig 5. Gate Charge

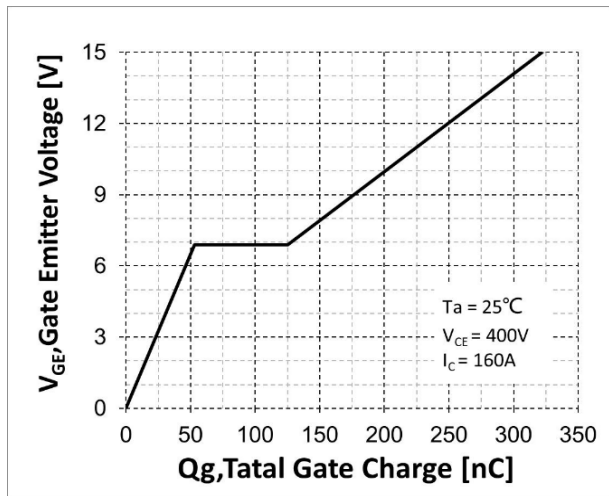


Fig 6. Typical Capacitance

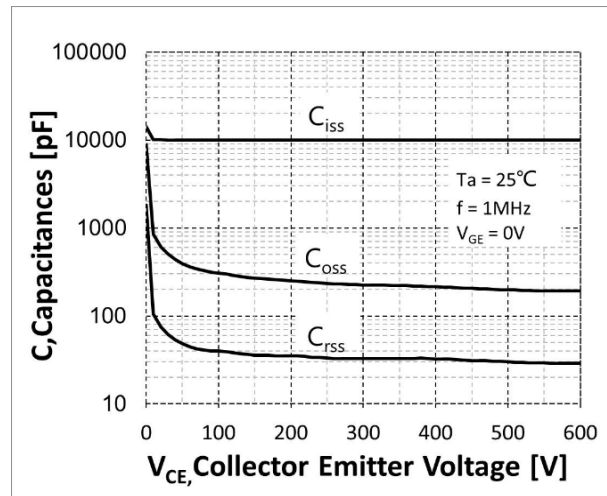


Fig 7. Vcesat vs. Junction Temperature

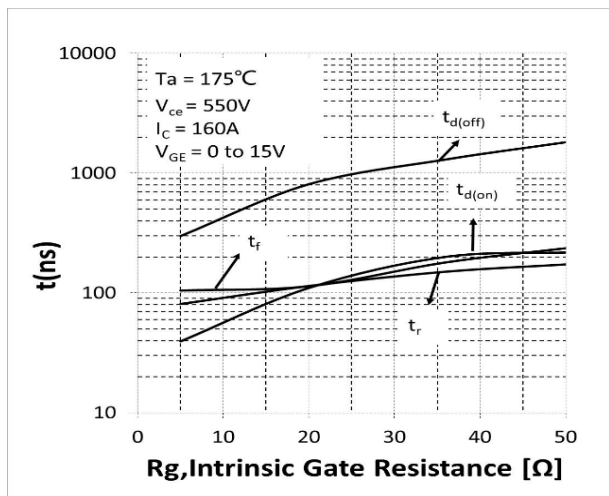


Fig 8. Typical Diode VF vs. IF Characteristic

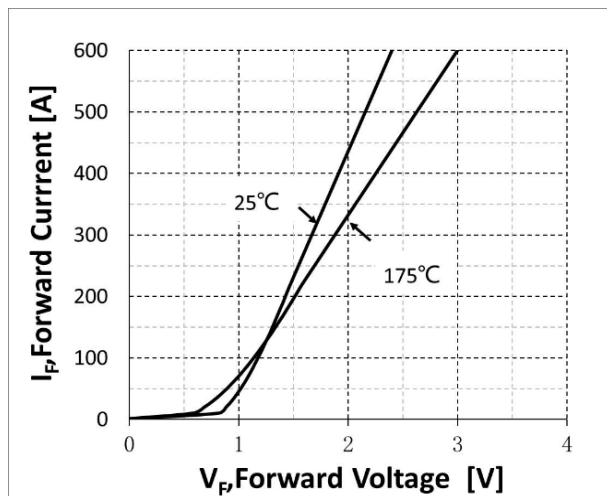


Fig 9. Threshold Voltage vs. Junction Temperature **Fig 10. Switching Energy Loss vs. Collector Current**

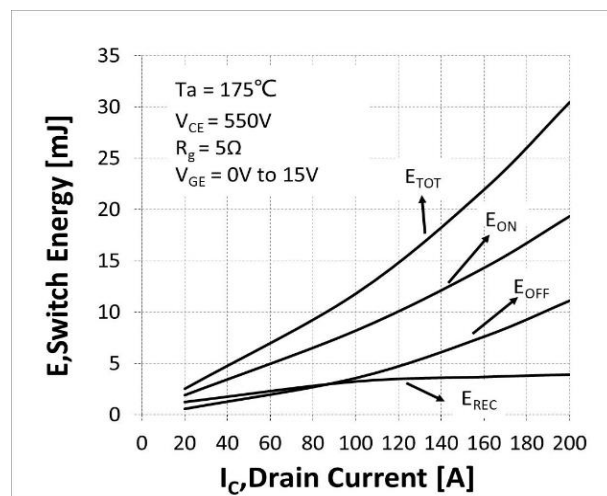
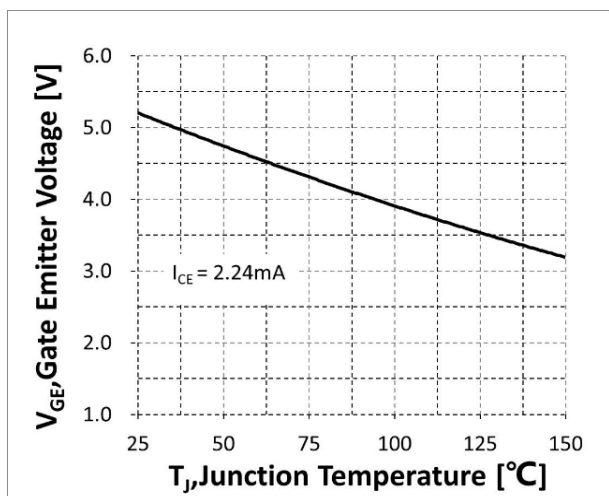


Fig 11. Switching Energy Loss vs. Gate Resistance

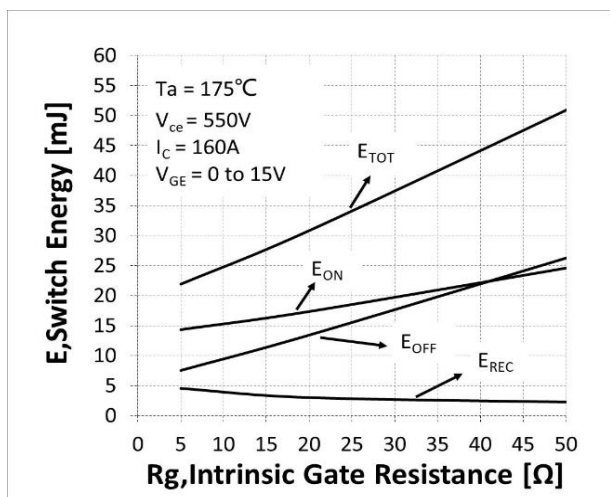


Fig 12. Transient Thermal Impedance IGBT

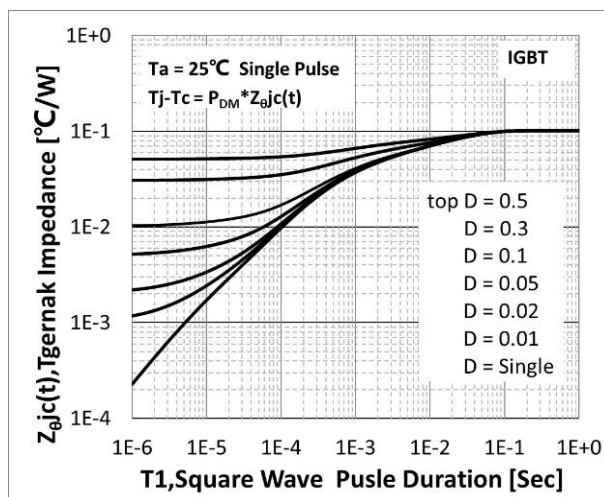


Fig 13. Transient Thermal Impedance Diode

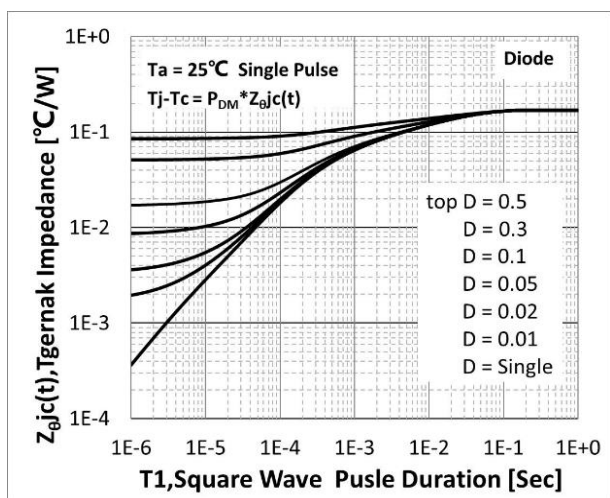


Fig 12. Switching Test Circuit & Waveforms

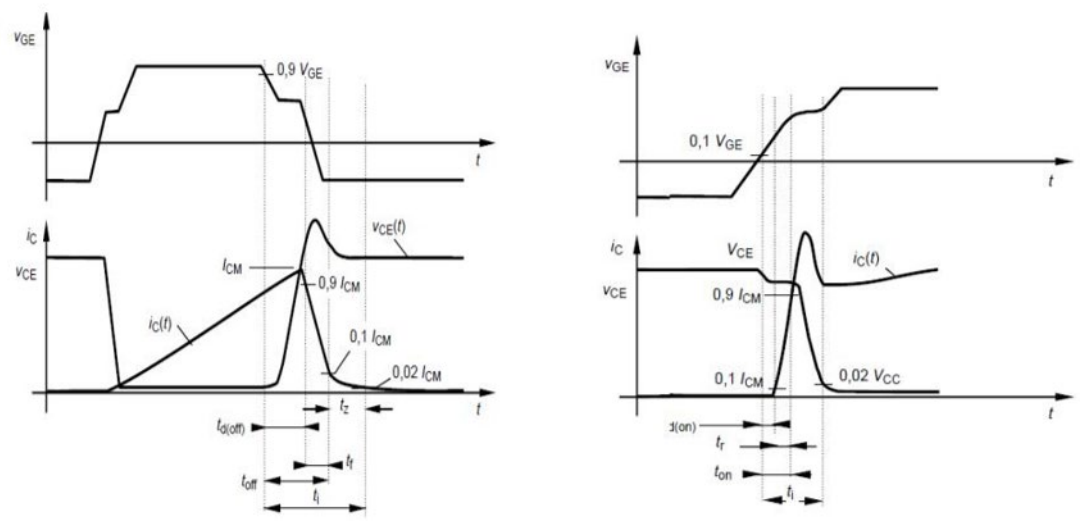
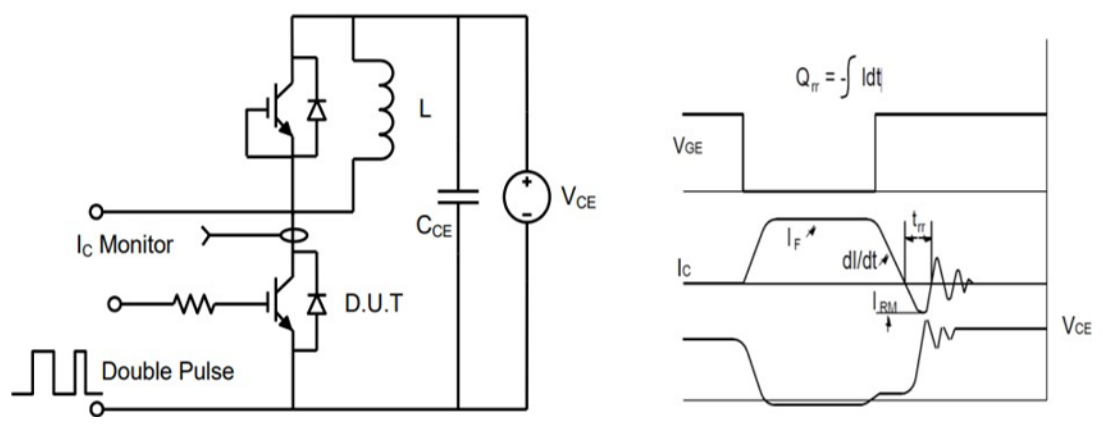
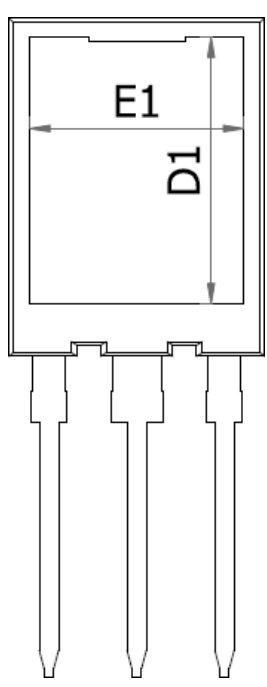
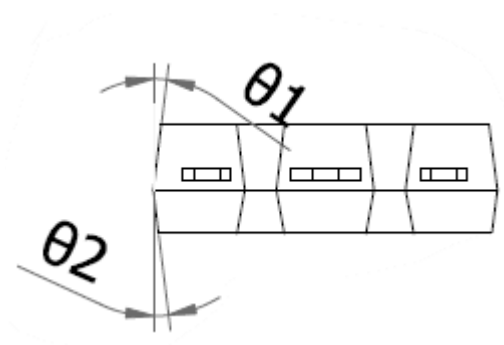
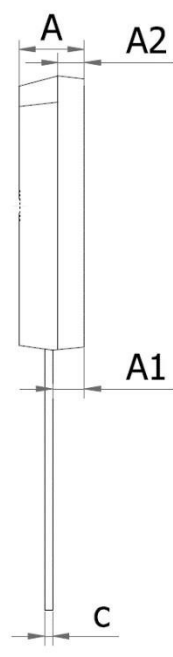
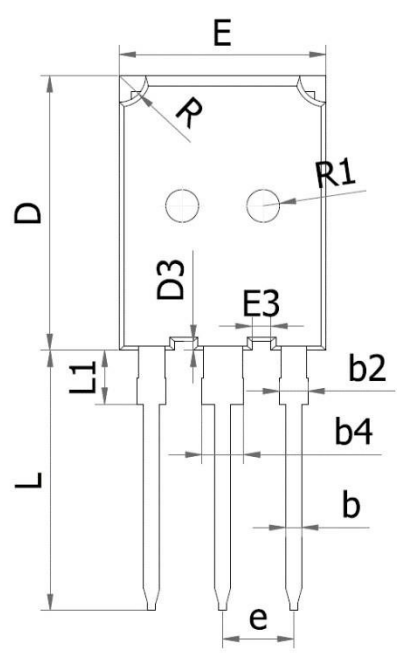


Fig 13. Diode Recovery Test Circuit & Waveforms




Package Outline



SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.23	2.43	2.63
A2	1.85	2.00	2.15
b	1.10	1.20	1.30
b2	1.90	2.10	2.20
b4	2.90	3.10	3.20
c	0.50	0.60	0.70
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
D3	0.53	0.68	0.83
E	15.50	15.80	16.10
E1	13.05	13.25	13.45
E3	1.30	1.45	1.60
e	5.44 BSC		
L	19.62	19.92	20.22
L1	---	---	4.30
R	1.85	2.00	2.15
R1	1.10	1.25	1.40
θ_1	5°	7°	9°
θ_2	5°	7°	9°


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