

700V GaN Power Transistor

Description

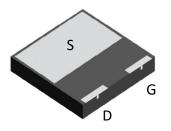
Product Summary					
V _{DS} (V)	$R_{DS(on)}(m\Omega)(Typ)$	I _D (A)			
700	320	5.4			

Feature

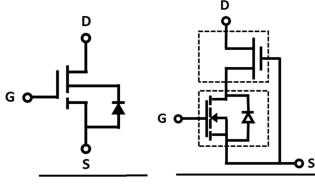
- > Easy to use, compatible with standard gate drivers
- ➤ Excellent Q_G x R_{DS(on)} figure of merit (FOM)
- ightharpoonup Low \mathbf{Q}_{RR} , no free-wheeling diode required
- Low switching loss
- > RoHS compliant and Halogen-free

Applications

- High efficiency power supplies
- > Telecom and datacom
- > Automotive
- Servo motors



DFN8080-3L (Bottom View)



Schematic Symbol

Cascode Device Structure

Absolute maximum rating@25°C

Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	700	V	
Gate-Source Voltage		V _{GS}	±20	V	
Transient Drain-Source Voltage ¹⁾		V _{TDS}	800	V	
Continuous Drain Current	T _C =25°C		5.4	А	
	T _C =100°C	- I _D	3.4		
D. I I D	T _C =25°C		16	Α	
Pulsed Drain Current (Pulse Width: 100μs)	T _C =100°C	I _{DM}	12		
Power Dissipation		P _D	18	W	
Soldering Peak Temperature		T _{CSOLD}	260	°C	
Operating Junction and Storage Temperature		T _{J,} T _{STG}	-55 to 150	°C	

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	7	-	°C/W
Thermal Resistance, Junction-to-Ambient ²⁾	$R_{\theta JA}$	-	50	-	°C/W

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Units
Statistic Characteristics							
Maximum Drain-Source Voltage	V _{DS-Max}	V _{GS} = 0V		700	-	-	V
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$		-	1000	-	V
Zone Cote Veltere Duein Comment	I _{DSS}	V _{DS} =700V, V _{GS} =0V	T _J =25°C	-	4		
Zero Gate Voltage Drain Current			T _J =150°C	-	30	-	μA
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±150	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$,	l _D = 500μA	1.1	1.8	2.5	V
Drain-Source On-State Resistance ³⁾	Б	V _{GS} =8V, I _D =4A	T _J =25°C	ı	320	400	mΩ
Dialii-Source Oil-State Resistance	$R_{DS(ON)}$		T _J =150°C	ı	640	-	
Dynamic Characteristics							
Input Capacitance	C _{lss}	$V_{DS} = 400V, V_{GS} = 0V,$ f = 1MHz		ı	288	-	pF
Output Capacitance	C _{oss}			ı	12	-	
Reverse Transfer Capacitance	C _{rss}			-	0.6	-	
Effective Output Capacitance, Energy Related	C _{o(er)}	V _{GS} = 0V, V _{DS} = 0-400V		ı	18	-	pF
Effective Output Capacitance, Time Related	C _{o(tr)}			ı	54	-	
Output Charge	Q _{oss}			-	21.7	-	nC
Turn-on Delay Time	t _{d(on)}	V _{DS} = 400V,I _D = 3A,		ı	32	-	ns
Turn-on Rise Time	t _r			-	6	-	
Turn-Off Delay Time	t _{d(off)}	$V_{GS} = 0-12V, R_G = 47\Omega$		ı	50	-	
Turn-Off Fall Time	t _f			-	5	-	
Total Gate Charge	Q_g	$V_{DS} = 400V, I_{D} = 3.4A,$ $V_{GS} = 0-8V$		1	5.8	-	
Gate-Source Charge	Q_{gs}			-	1.7	-	nC
Gate-Drain Charge	Q_{gd}			-	2.5	-	
Reverse Diode Characteristics							
	V _{SD}	V _{GS} =0V,	I _S =1.7A	-	1.2	-	
Diode Forward Voltage		V _{GS} =0V,	T _J =25°C	-	1.6	-	V
		I _S =3A	T _J =150°C	-	2.3	-	
Reverse Recovery Time	t _{rr}	V _{GS} =0V		-	14	-	ns
Reverse Recovery Charge	Q _{rr}	- V _{DD} =400V, di/dt=1000A/μs		-	21.7	-	μC

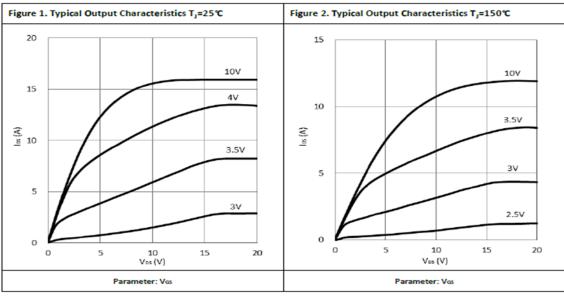
Notes:

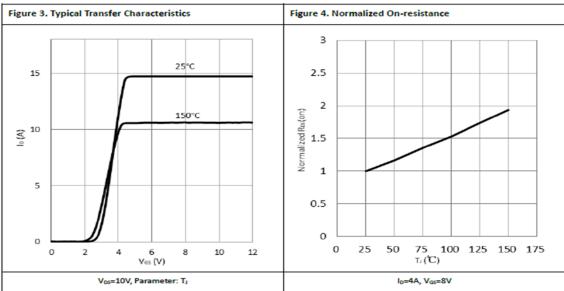
^{1.} Off-state spike duty cycle < 0.01, spike duration < 2μs

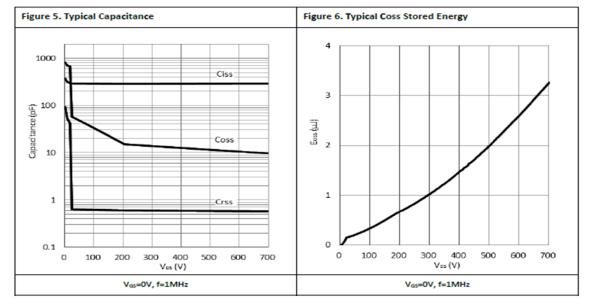
^{2.} Device on one layer epoxy PCB for drain connection (vertical and without air stream cooling, with 6cm²copper area and 70µm thickness)

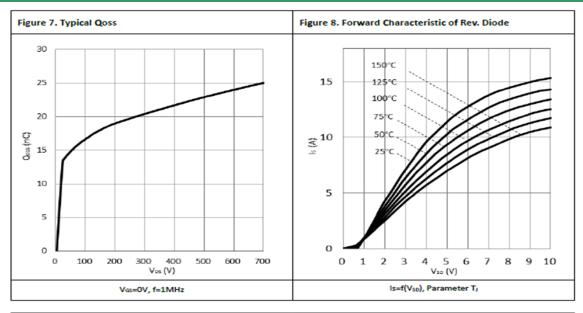
^{3.} Dynamic on-resistance; see Figure 18 and 19 for test circuit and configurations

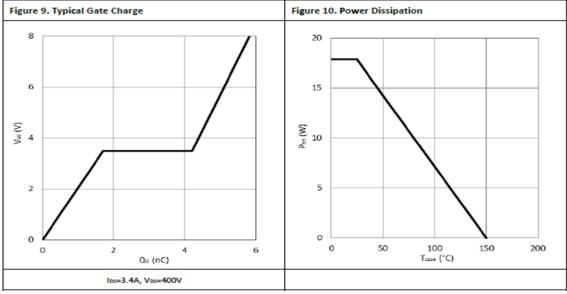
Typical Characteristics

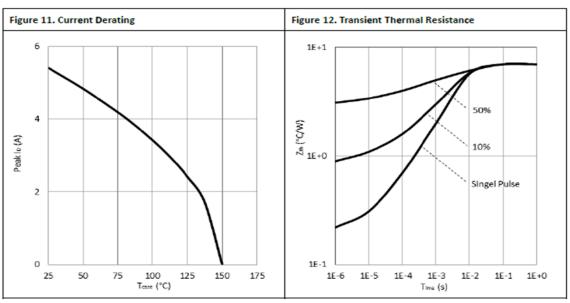


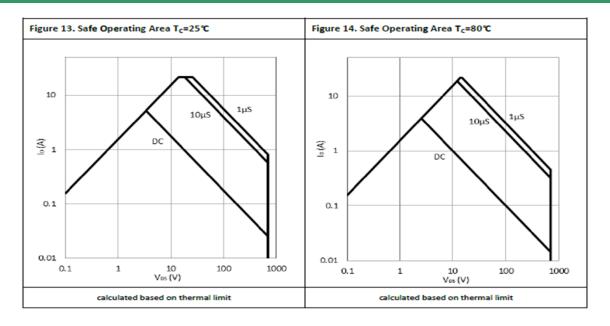




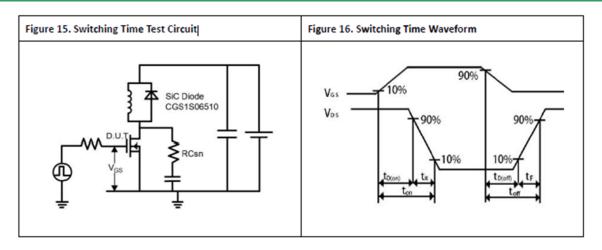


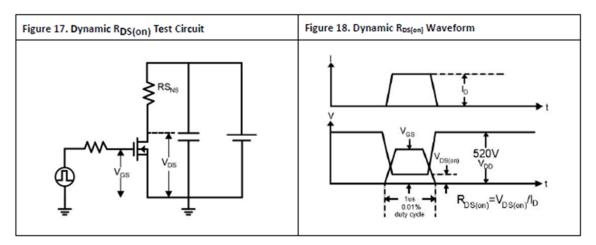


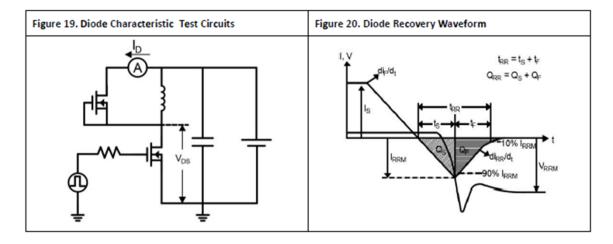




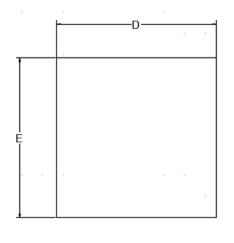
Test Circuits and Waveforms



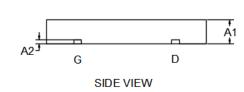


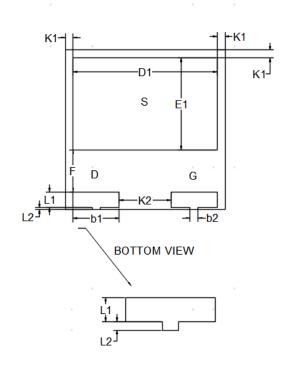


Product Dimension (DFN8080-3L)









CVMDOL	Millimeter				
SYMBOL	Min	Nom	Max		
A1	0.80	0.90	1.15		
A2	0.19	0.203	0.22		
b1	2.20	2.30	2.40		
b2	0.80REF				
D	7.90	8.00	8.10		
Е	7.90	8.00	8.10		
D1	6.90	7.20	7.50		
E1	4.40	4.60	4.80		
L1	0.70	0.80	0.90		
L2	0.12REF				
K1	0.30	0.40	0.50		
K2	2.50	2.60	2.70		
F	2.05	2.15	2.35		

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