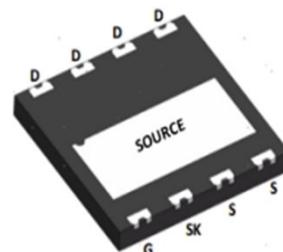


Description

Product Summary		
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ)	I _D (A)
700	180	10.4



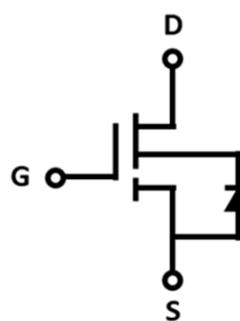
Feature

- Easy to use, compatible with standard gate drivers
- Excellent Q_G × R_{DS(on)} figure of merit (FOM)
- Low Q_{RR}, no free-wheeling diode required
- Low switching loss
- RoHS compliant and Halogen-free

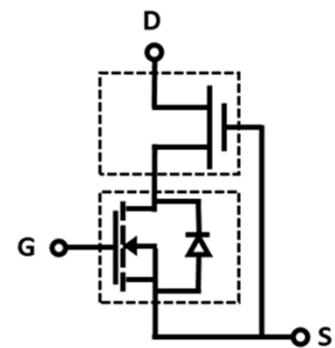
Applications

- High efficiency power supplies
- Telecom and datacom
- Automotive
- Servo motors

DFN8080-8L (Bottom View)



Schematic Symbol



**Cascode
Device Structure**

Absolute maximum rating@25°C

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	700	V
Gate-Source Voltage	V _{GS}	±20	V
Transient Drain-Source Voltage ¹⁾	V _{TDS}	800	V
Continuous Drain Current	T _C =25°C	10.4	A
		6.5	
Pulsed Drain Current (Pulse Width: 100μs)	T _C =25°C	31	A
		23	
Power Dissipation	P _D	37	W
Soldering Peak Temperature	T _{CSOLD}	260	°C
Operating Junction and Storage Temperature	T _J , T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	-	3.4	-	°C/W
Thermal Resistance, Junction-to-Ambient ²⁾	R _{θJA}	-	50	-	°C/W

700V GaN Power Transistor

PGC8FN70R180B

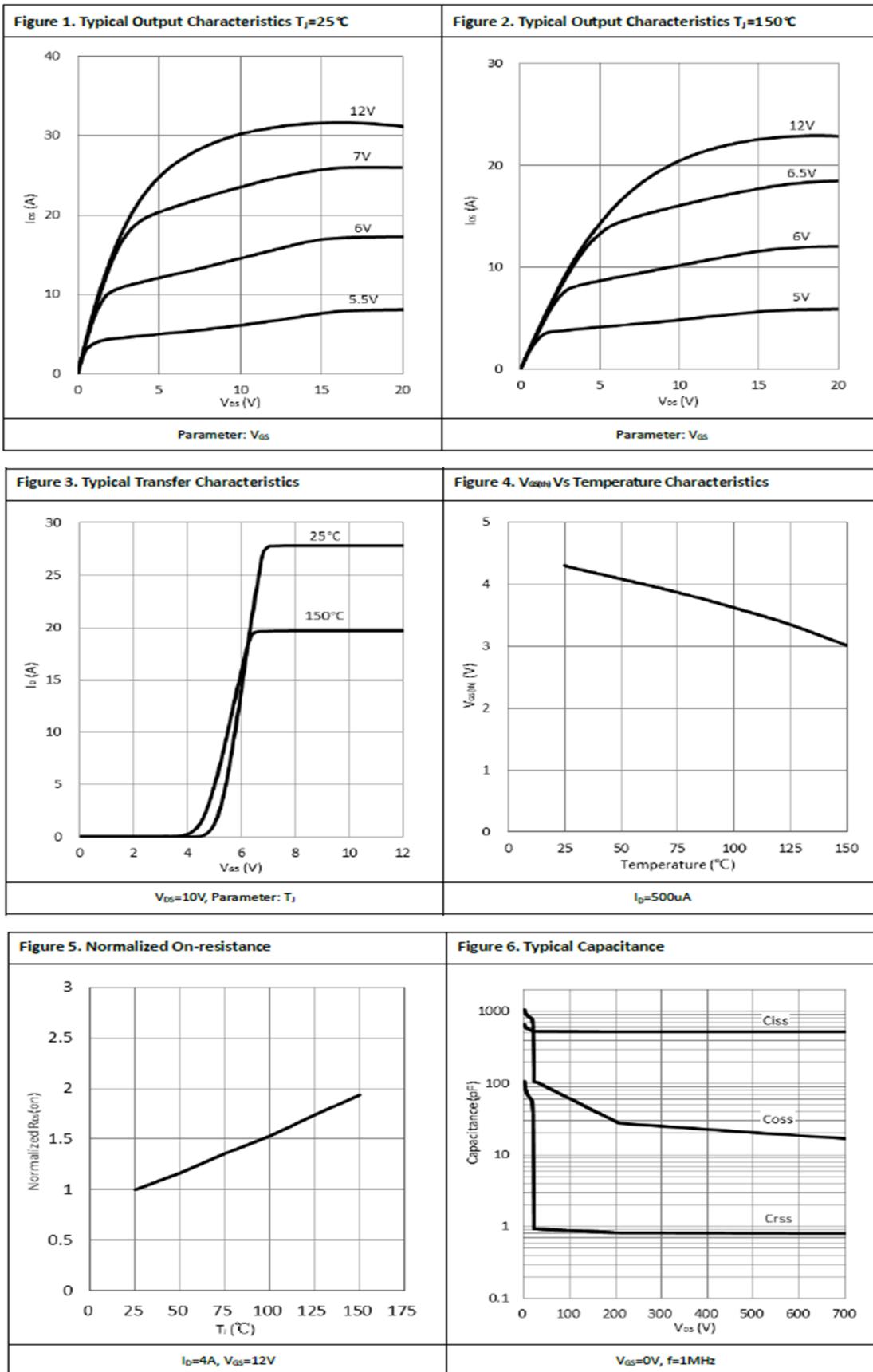
Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Units
Statistic Characteristics							
Maximum Drain-Source Voltage	V _{DS-Max}	V _{GS} = 0V		700	-	-	V
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250µA		-	1000	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =700V, V _{GS} =0V	T _J =25°C	-	9	20	µA
			T _J =150°C	-	50	-	
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V		-	-	±150	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 500µA		3	4	5	V
Gate threshold voltage temperature coefficient	△V _{GS(th)} /T _J	V _{DS} = V _{GS} , I _D = 500µA		-	-10.7	-	mV/°C
Drain-Source On-State Resistance ³⁾	R _{DS(ON)}	V _{GS} =12V, I _D =4A	T _J =25°C	-	180	225	mΩ
			T _J =150°C	-	360	-	
Dynamic Characteristics							
Input Capacitance	C _{iss}	V _{DS} = 400V, V _{GS} = 0V, f = 1MHz		-	506	-	pF
Output Capacitance	C _{oss}			-	22	-	
Reverse Transfer Capacitance	C _{rss}			-	0.8	-	
Effective Output Capacitance, Energy Related	C _{o(er)}	V _{GS} = 0V, V _{DS} = 0-400V		-	33	-	pF
Effective Output Capacitance, Time Related	C _{o(tr)}			-	79	-	
Output Charge	Q _{oss}			-	32	-	nC
Turn-on Delay Time	t _{d(on)}	V _{DS} = 400V, I _D = 6A, V _{GS} = 0-12V, R _G = 47Ω		-	60	-	ns
Turn-on Rise Time	t _r			-	12	-	
Turn-Off Delay Time	t _{d(off)}			-	80	-	
Turn-Off Fall Time	t _f			-	10	-	
Total Gate Charge	Q _g	V _{DS} = 400V, I _D = 6.5A, V _{GS} = 0-12V		-	15.8	-	nC
Gate-Source Charge	Q _{gs}			-	3.8	-	
Gate-Drain Charge	Q _{gd}			-	5.5	-	
Reverse Diode Characteristics							
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =3.2A		-	1.3	-	V
		V _{GS} =0V, I _S =6.5A	T _J =25°C	-	1.9	-	
			T _J =150°C	-	2.7	-	
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _S =6.5A, V _{DD} =400V, di/dt=1000A/µs		-	18	-	ns
Reverse Recovery Charge	Q _{rr}			-	32	-	µC

Notes:

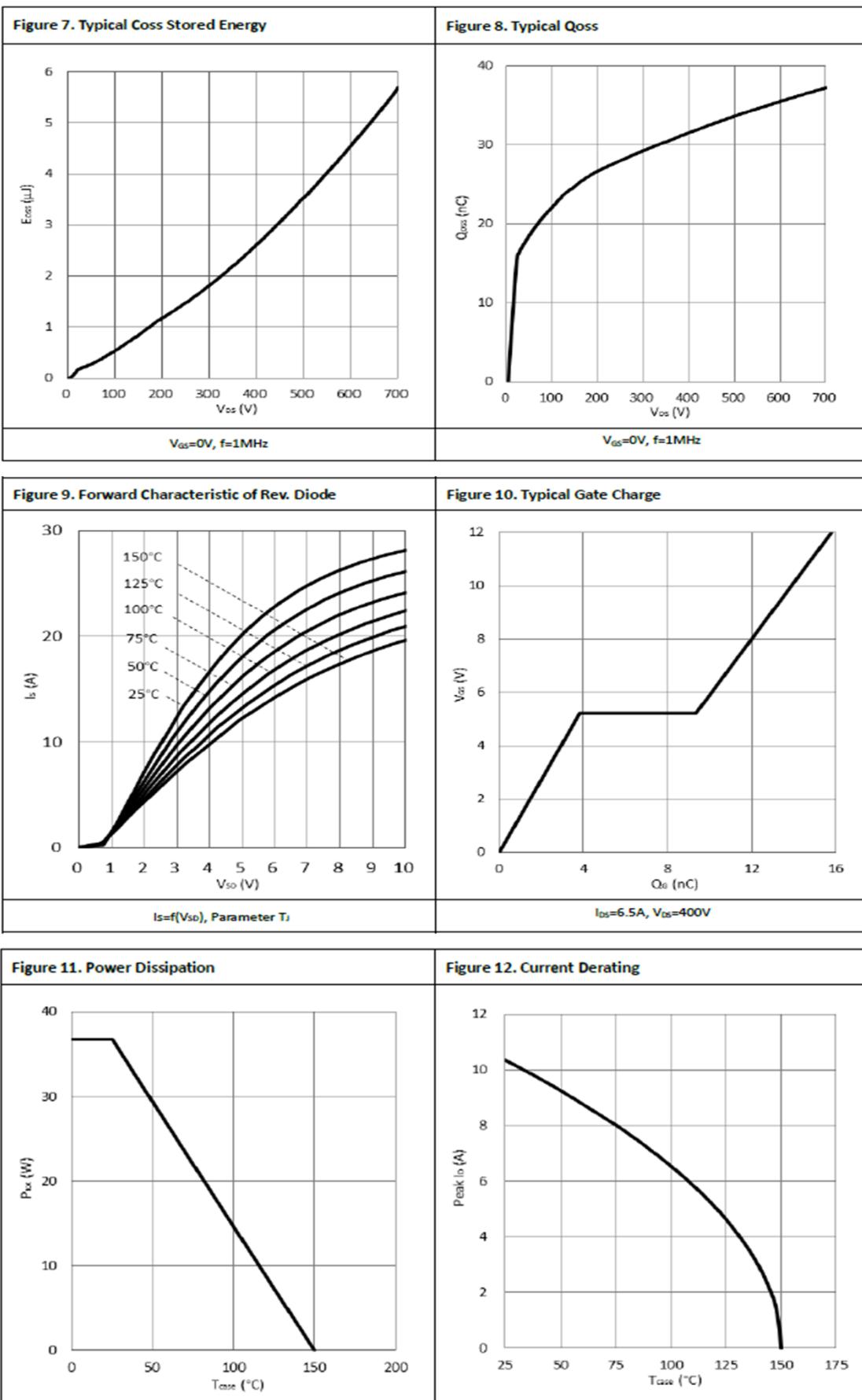
1. Off-state spike duty cycle < 0.01, spike duration < 2µs
2. Device on one layer epoxy PCB for drain connection (vertical and without air stream cooling, with 6cm²copper area and 70µm thickness)
3. Dynamic on-resistance; see Figure 19 and 20 for test circuit and configurations

Typical Characteristics



700V GaN Power Transistor

PGC8FN70R180B



700V GaN Power Transistor

PGC8FN70R180B

Figure 13. Transient Thermal Resistance

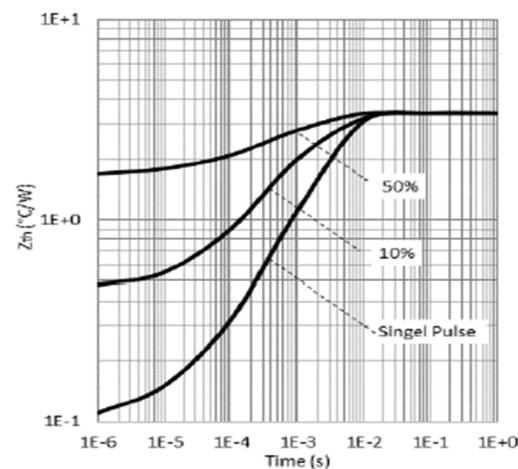


Figure 14. Safe Operating Area $T_c=25^{\circ}\text{C}$

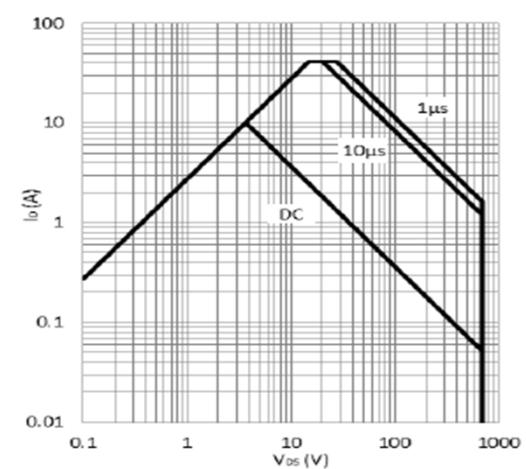
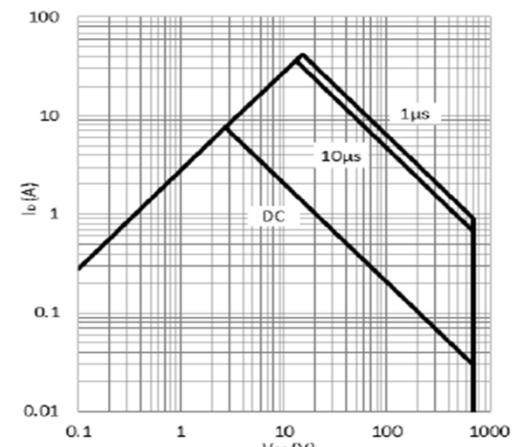


Figure 15. Safe Operating Area $T_c=80^{\circ}\text{C}$



Test Circuits and Waveforms

Figure 16. Switching Time Test Circuit

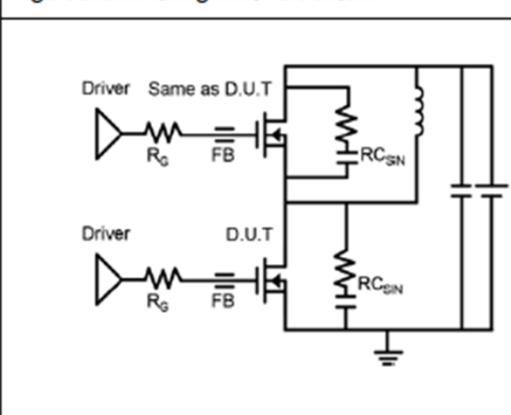


Figure 17. Switching Time Waveform

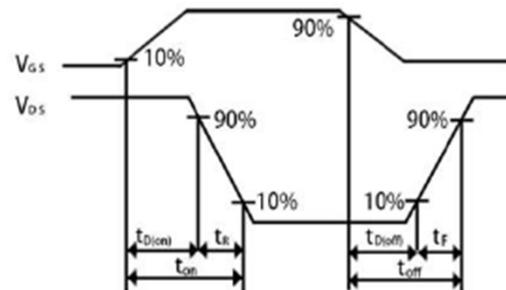


Figure 18. Dynamic R_{DS(on)} Test Circuit

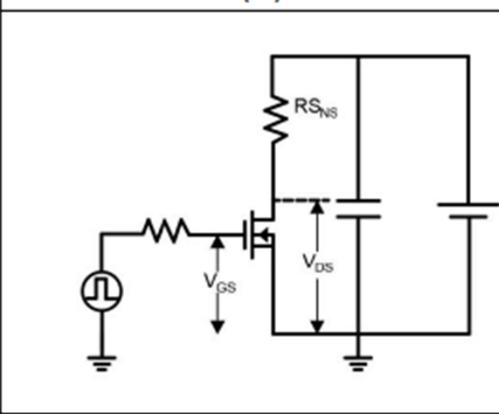


Figure 19. Dynamic R_{DS(on)} Waveform

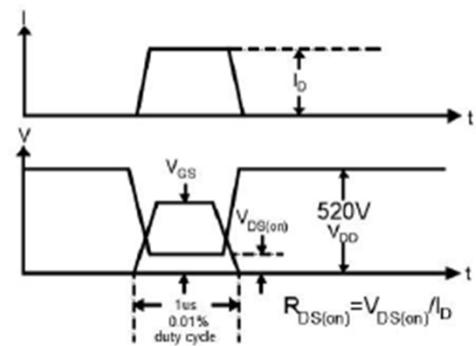


Figure 20. Diode Characteristic Test Circuits

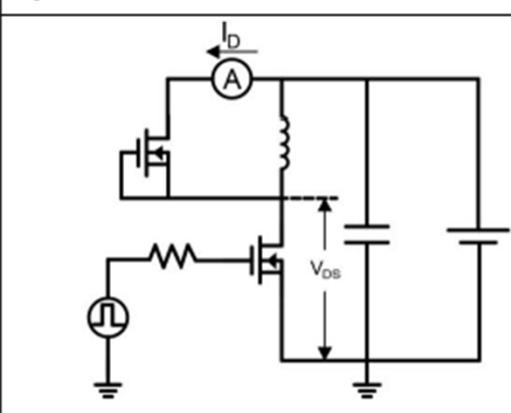
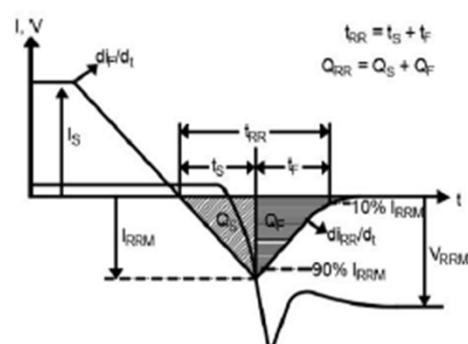
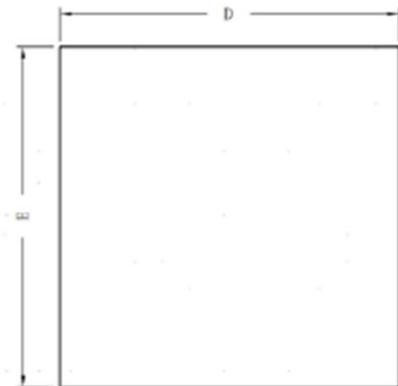


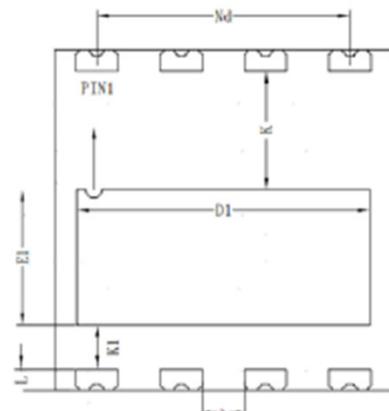
Figure 21. Diode Recovery Waveform



Product Dimension (DFN8080-8L)



TOP VIEW



BOTTOM VIEW



SIDE VIEW

SYMBOL	Millimeter		
	Min	Nom	Max
A	0.80	0.90	1.15
A1	0	0.02	0.05
c	—	0.20	—
b	0.90	1.00	1.10
D	7.90	8.00	8.10
D1	6.85	6.95	7.05
E	7.90	8.00	8.10
E1	3.10	3.20	3.30
e	2.00BSC		
Nd	6.00BSC		
K	2.70	2.80	2.90
K1	0.90	1.00	1.10
L	0.40	0.50	0.60

IMPORTANT NOTICE

 and **Prisemi**[®] are registered trademarks of **Prisemi Electronics Co., Ltd** (Prisemi), Prisemi reserves the right to make changes without further notice to any products herein. Prisemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Prisemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in Prisemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Prisemi does not convey any license under its patent rights nor the rights of others. The products listed in this document are designed to be used with ordinary electronic equipment or devices, Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

Website: <http://www.prisemi.com>

For additional information, please contact your local Sales Representative.

©Copyright 2009, Prisemi Electronics

 **Prisemi**[®] is a registered trademark of Prisemi Electronics.

All rights are reserved.